| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|---|---|---|---|---|---|---|
| L1 | 0 | mapper pulses quantizer | USPAT | WITH | OFF | 2005/02/17 12:48 |
| L2 | 0 | mapper pulses quantizer | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | WITH | OFF | 2005/02/17 12:49 |
| L3 | 0 | mapper pulse quantizer | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | WITH | ON | 2005/02/17 12:49 |
| L4 | 0 | map pulse quantizer | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | WITH | ON | 2005/02/17 12:50 |
| L5 | 9 | map pulse quantizer | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | SAME | ON | 2005/02/17 12:50 |
| L6 | 82 | map pulse quantiz$ | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | SAME | ON | 2005/02/17 12:51 |
| L7 | 0 | map pulse quantiz$ | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | NEAR | ON | 2005/02/17 12:51 |
| L8 | 0 | mapping pulse quantiz$ | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | NEAR | ON | 2005/02/17 12:51 |
| L9 | 0 | map pulse quantiz$ | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | NEAR | ON | 2005/02/17 12:51 |

| | | | | | | |
|---|---|---|---|---|---|---|
| L10 | 0 | map pulse audio | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | NEAR | ON | 2005/02/17 12:52 |
| L11 | 77 | map pulse audio | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | SAME | ON | 2005/02/17 12:52 |
| L12 | 10 | map pulse audio | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | WITH | ON | 2005/02/17 12:56 |
| L13 | 14711 | ((375/371,372,373,377,293,294, 327,355,363,362,359,375,376) or (370/505,516,503,504,506,518, 509) or (341/144,155,164,165,67, 61) or (365/189.01,189.04,189. 05) or (369/47.19)).CCLS. | USPAT | OR | OFF | 2005/02/17 12:57 |

IEEE HOME | SEARCH IEEE | SHOP | WEB ACCOUNT | CONTACT IEEE                      ◈IEEE

Membership   Publications/Services   Standards   Conferences   Careers/Jobs

**IEEE** Xplore®
RELEASE 1.6

IEEE Xplore®
1 Million Documents
1 Million Users

Help   FAQ   Terms·  IEEE Peer Review     | Quick Links                    ▼ |        » Se:

Welcome to IEEE Xplore*

○ Home
○ What Can
   I Access?
○ Log-out

Tables of Contents

○ Journals
   & Magazines
○ Conference
   Proceedings
○ Standards

Search

○ By Author
○ Basic
○ Advanced
○ CrossRef

Member Services

○ Join IEEE
○ Establish IEEE
   Web Account
○ Access the
   IEEE Member
   Digital Library

○ Access the
   IEEE Enterprise
   File Cabinet

🖨 Print Format

Your search matched **9** of **1128145** documents.
A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance Descending** order.

**Refine This Search:**
You may refine your search by editing the current search expression or enteri new one in the text box.

| mapper<and>pulse |        | Search |

☐ Check to search within this result set

**Results Key:**
**JNL** = Journal or Magazine   **CNF** = Conference   **STD** = Standard
🖥 = Your access to full-text

___

1 **Absolute calibration of the Landsat Thematic Mapper using the Inte Calibrator**
*Helder, D.; Boucyk, W.; Morfitt, R.;*
Geoscience and Remote Sensing Symposium Proceedings, 1998. IGARSS '98.
IEEE International , Volume: 5 , 6-10 July 1998
Pages:2716 - 2718 vol.5

[Abstract]   [PDF Full-Text (244 KB)]   **IEEE CNF**

___

2 **Bandwidth-efficient trellis coded modulation schemes**
*Ramseier, S.;*
Communications, 1990. ICC 90, Including Supercomm Technical Sessions.
SUPERCOMM/ICC '90. Conference Record., IEEE International Conference on ,
19 April 1990
Pages:1517 - 1521 vol.4

[Abstract]   [PDF Full-Text (376 KB)]   **IEEE CNF**

___

3 **Coded modulation using superimposed binary codes**
*Xiao Ma; Li Ping;*
Information Theory, IEEE Transactions on , Volume: 50 , Issue: 12 , Dec. 200
Pages:3331 - 3343

[Abstract]   [PDF Full-Text (496 KB)]   **IEEE JNL**

___

4 **Multipurpose high-coding-gain 0.8-μm BiCMOS VLSIs for high-speec multilevel trellis-coded modulation**
*Aikawa, S.; Nakamura, Y.; Takanashi, H.;*
Solid-State Circuits, IEEE Journal of , Volume: 26 , Issue: 11 , Nov. 1991

Pages:1700 - 1707

[Abstract]    [PDF Full-Text (640 KB)]    **IEEE JNL**

---

5 **Combined iterative demapping and decoding for coded UWB-IR syst**
*Takizawa, K.; Kohno, R.;*
Ultra Wideband Systems and Technologies, 2003 IEEE Conference on , 16-19
2003
Pages:423 - 427

[Abstract]    [PDF Full-Text (380 KB)]    **IEEE CNF**

---

6 **Predictive analog to digital conversion of Doppler ultrasound signals**
*Boe, S.; Kristoffersen, K.;*
Biomedical Engineering, IEEE Transactions on , Volume: 42 , Issue: 3 , March

Pages:260 - 268

[Abstract]    [PDF Full-Text (760 KB)]    **IEEE JNL**

---

7 **Generalized unified construction of space-time codes with optimal r:
diversity tradeoff**
*Hsiao-feng Lu; Kumar, P.V.;*
Information Theory, 2004. ISIT 2004. Proceedings. International Symposium
on , 27 June-2 July 2004
Pages:95

[Abstract]    [PDF Full-Text (244 KB)]    **IEEE CNF**

---

8 **Laser altimetry waveform measurement of vegetation canopy struct**
*Harding, D.J.; Blair, J.B.; Garvin, J.B.; Lawrence, W.T.;*
Geoscience and Remote Sensing Symposium, 1994. IGARSS '94. 'Surface and
Atmospheric Remote Sensing: Technologies, Data Analysis and Interpretation'
International , Volume: 2 , 8-12 Aug. 1994
Pages:1251 - 1253 vol.2

[Abstract]    [PDF Full-Text (464 KB)]    **IEEE CNF**

---

9 **150 KGate general-purpose high-coding-gain TCM VLSIs for high-sp
multi-level QAM systems**
*Aikawa, S.; Nakamura, Y.; Takanashi, H.;*
Global Telecommunications Conference, 1990, and Exhibition. 'Communicatior
Connecting the Future', GLOBECOM '90., IEEE , 2-5 Dec. 1990
Pages:1963 - 1967 vol.3

[Abstract]    [PDF Full-Text (412 KB)]    **IEEE CNF**

---

Home | Log-out | Journals | Conference Proceedings | Standards | Search by Author | Basic Search | Advanced Search | Join IEEE | Web Account |
New this week | OPAC Linking Information | Your Feedback | Technical Support | Email Alerting | No Robots Please | Release Notes | IEEE Online
Publications | Help | FAQ| Terms | Back to Top

**PORTAL**

US Patent & Trademark Office

Search:   ● The ACM Digital Library   ○ The Guide

+mapper +pulse

THE ACM DIGITAL LIBRARY

Feedback   Report a problem   Satisfaction survey

Terms used **mapper pulse**                                    Found **6** of **150,138**

| Sort results by | relevance ▾ | ● Save results to a Binder | Try an Advanced Search |
| Display results | expanded form ▾ | ? Search Tips | Try this search in The ACM Guide |
| | | ☐ Open results in a new window | |

Results 1 - 6 of 6

Relevance scale ☐ ☐ ☐ ☐ ☐

**1** Timing-driven partitioning for two-phase domino and mixed static/domino implementations
Min Zhao, Sachin S. Sapatnekar
November 1999 **Proceedings of the 1999 IEEE/ACM international conference on Computer-aided design**
Full text available: 📄 pdf(114.02 KB)     Additional Information: full citation, abstract, references, index terms

> Domino logic is a high-performance circuit configuration that is usually embedded in static logic environment and tightly coupled with the clocking scheme. In this paper, the timing-driven partitioning algorithms that partition a logic network between (1) static and domino implementations, and (2) the phases of a two-phase clock, are provided. In addition, an efficient static mapping algorithm is described.

**2** Synthesis tools for mixed-signal ICs: progress on frontend and backend strategies
L. Richard Carley, Georges G. E. Gielen, Rob A. Rutenbar, Willy M. C. Sansen
June 1996 **Proceedings of the 33rd annual conference on Design automation**
Full text available: 📄 pdf(91.08 KB)     Additional Information: full citation, references, citings, index terms

**3** Special session on reconfigurable computing: Physical design methodologies for performance predictability and manufacturability
Ricardo Reis, Fernanda Lima Kastensmidt, José Luís Güntzel
April 2004 **Proceedings of the first conference on computing frontiers on Computing frontiers**
Full text available: 📄 pdf(2.57 MB)     Additional Information: full citation, abstract, references, index terms

> The Physical Design Methodology of Integrated Systems is increasing its relevance in deep submicron technologies due to the appearance of new problems related to electrical behavior and performance predictability. This paper presents some techniques to improve reliability and manufacturability by the use of some layout strategies. One main approach is the search of regular solutions as the use of a layout composed by a matrix of cells. It is discussed the effects of layout strategies in the desi ...

> **Keywords:** DFM, design methodologies, layout, physical design, regularity

**4** Group G: modeling and evaluation methodology: Constraint-guided dynamic

reconfiguration in sensor networks

Sachin Kogekar, Sandeep Neema, Brandon Eames, Xenofon Koutsoukos, Akos Ledeczi, Miklos Maroti

April 2004 **Proceedings of the third international symposium on Information processing in sensor networks**

Full text available: pdf(388.61 KB)     Additional Information: full citation, abstract, references, index terms

This paper presents an approach for dynamic software reconfiguration in sensor networks. Our approach utilizes explicit models of the design space of the embedded application. The design space is captured by formally modeling all the software components, their interfaces, and their composition. System requirements are expressed as formal constraints on QoS parameters that are measured at runtime. Reconfiguration is performed by transitioning from one point of the operation space to another based ...

**Keywords**: design space exploration, runtime/dynamic software reconfiguration

**5** Special session on reconfigurable computing: Adaptive architectures for an OTN processor: reducing design costs through reconfigurability and multiprocessing

Tudor Murgan, Mihail Petrov, Mateusz Majer, Peter Zipf, Manfred Glesner, Ulrich Heinkel, Joerg Pleickhardt, Bernd Bleisteiner

April 2004 **Proceedings of the first conference on computing frontiers on Computing frontiers**

Full text available: pdf(1.01 MB)     Additional Information: full citation, abstract, references, citings, index terms

The standardisation process of Optical Transport Networks generally spans a long period of time. For providers intending to be present early on the market, this implies costly design re-spins if the wrong "flavour" of the protocol standard has been implemented. Extending a protocol processing device through application specific reconfigurable elements or multiprocessor units augment its flexibility. Thus, the architecture can be upgraded to standard updates or changes not even considered at desi ...

**Keywords**: ITU-T G.709, multiprocessor and reconfigurable architectures, optical transport networks, standard upgrades

**6** Image transfer: an end-to-end design

Charles J. Turner, Larry L. Peterson

October 1992 **ACM SIGCOMM Computer Communication Review , Conference proceedings on Communications architectures & protocols**, Volume 22 Issue 4

Full text available: pdf(1.23 MB)     Additional Information: full citation, abstract, references, citings, index terms

The transfer of digital images between data archives and scientific workstations is likely to consume a significant amount of network bandwidth in the very near future. This paper examines the image transfer problem from an end-to-end perspective, that is, it describes a complete image transfer protocol that takes into account both the nature of digital imagery and the properties of the underlying network. Specifically, it describes a simple algorithm for encoding images into network packet ...

Results 1 - 6 of 6